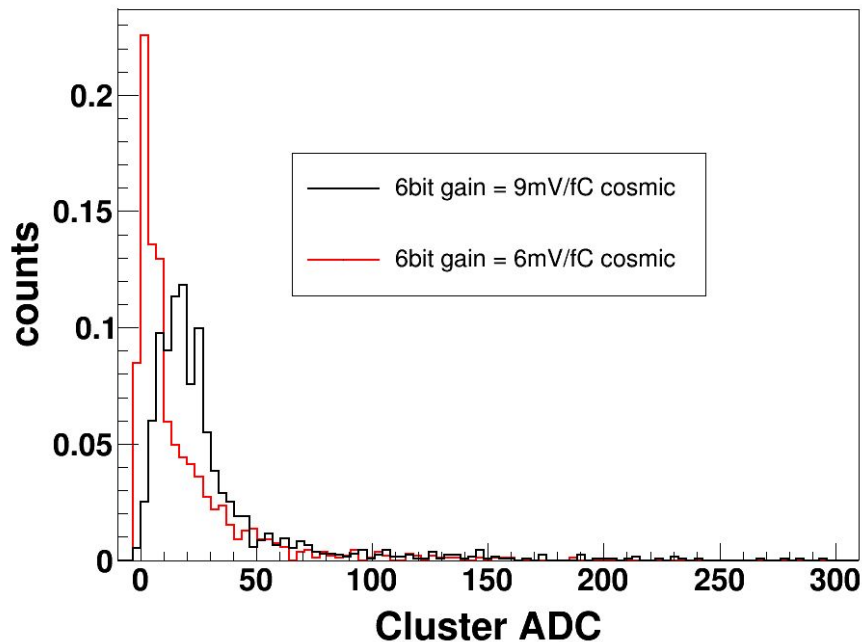


VMM SNR

Cosmic Signal ADC distribution – Sanity Check

- Larger gain setting can increase signal amplitude significantly
- GEM HV = 3900 V

cluster ADC distribution

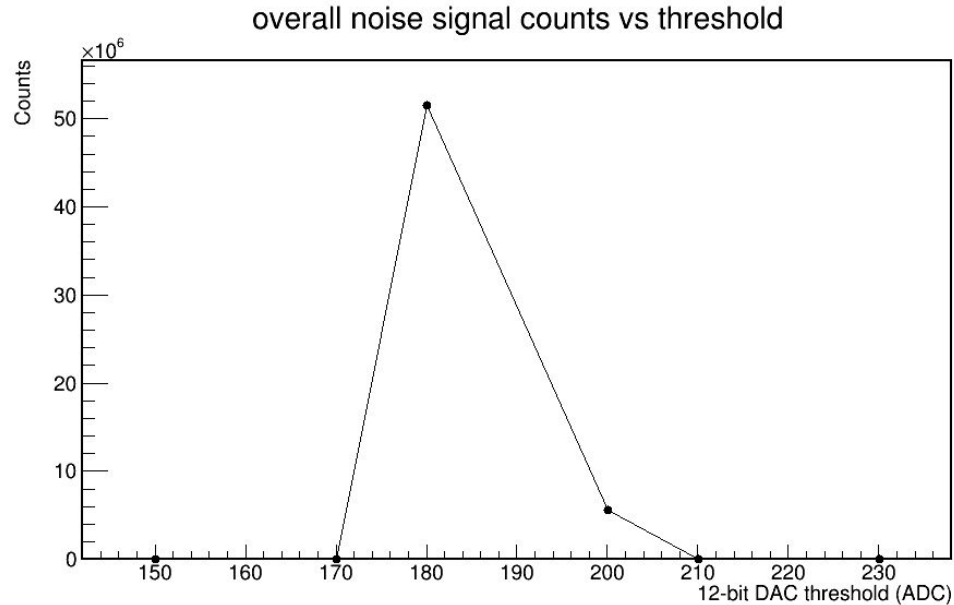


Determine Noise Amplitude

- VMM has peak finding implemented, when a signal pass through the threshold, the channel will be triggered
- Under normal operation, we set a proper threshold to cut away noise signals
- To study noise signal, we lower that threshold, and check the noise signal amplitude
- When threshold is too low, VMM will be in constant triggered mode, leading to no effective trigger

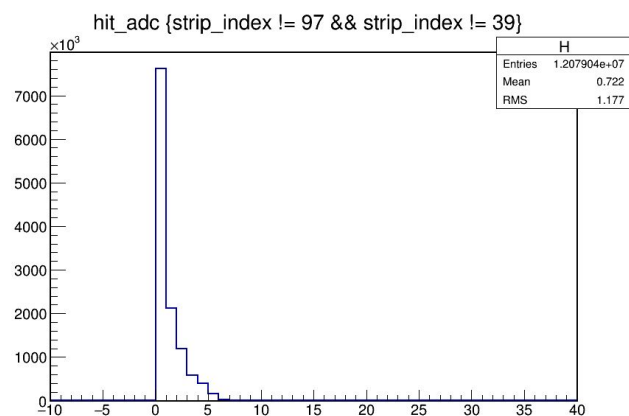
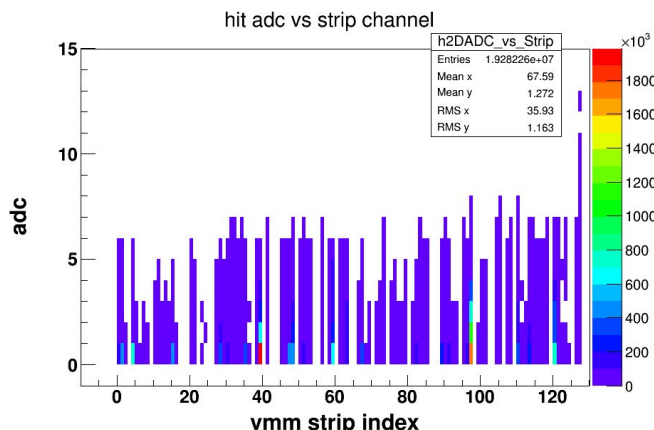
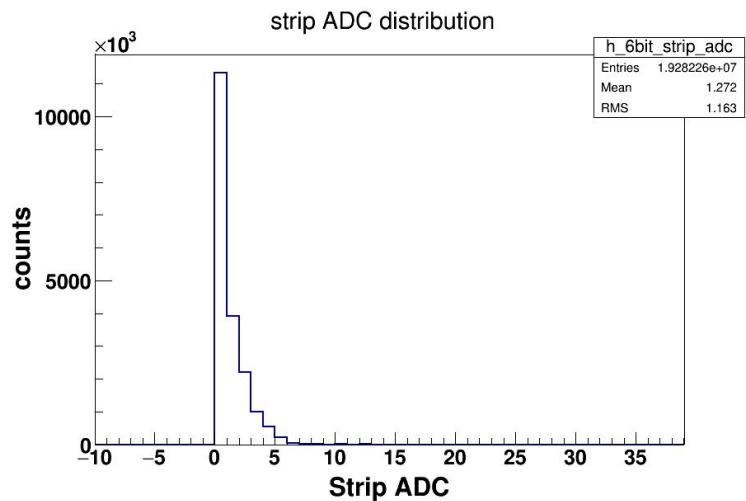
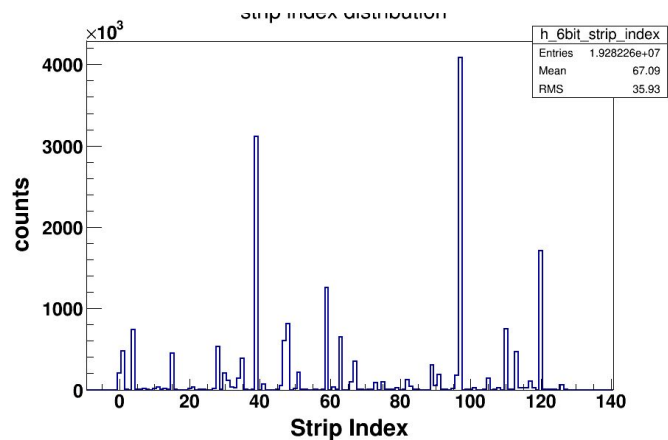
Determine Noise Amplitude

- 100000 triggers
- 6-bit mode, gain = 9 mV/fC



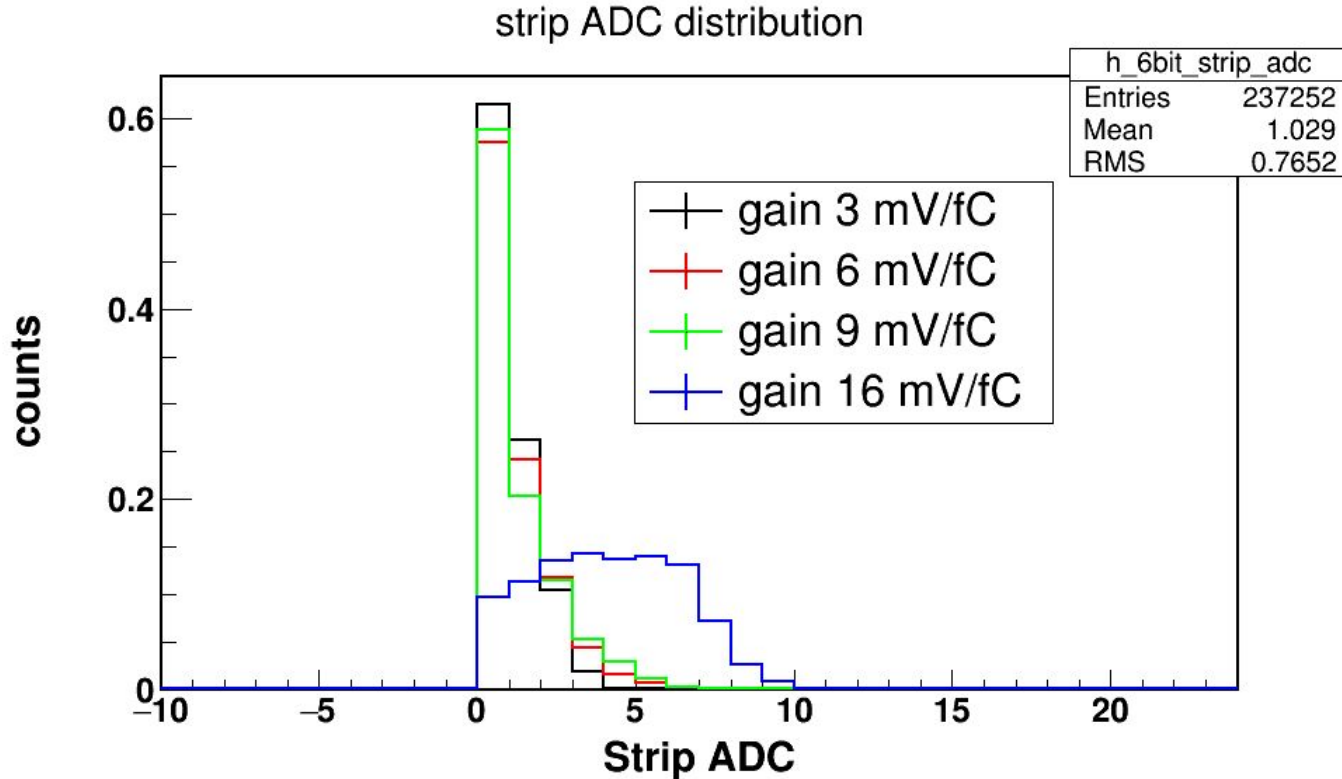
- Global threshold DAC = 180, gain 9 mV/fC

Noise Signal Amplitude



Noise amplitude with VMM Gain setting

- Noise signal amplitude is independent of gain setting

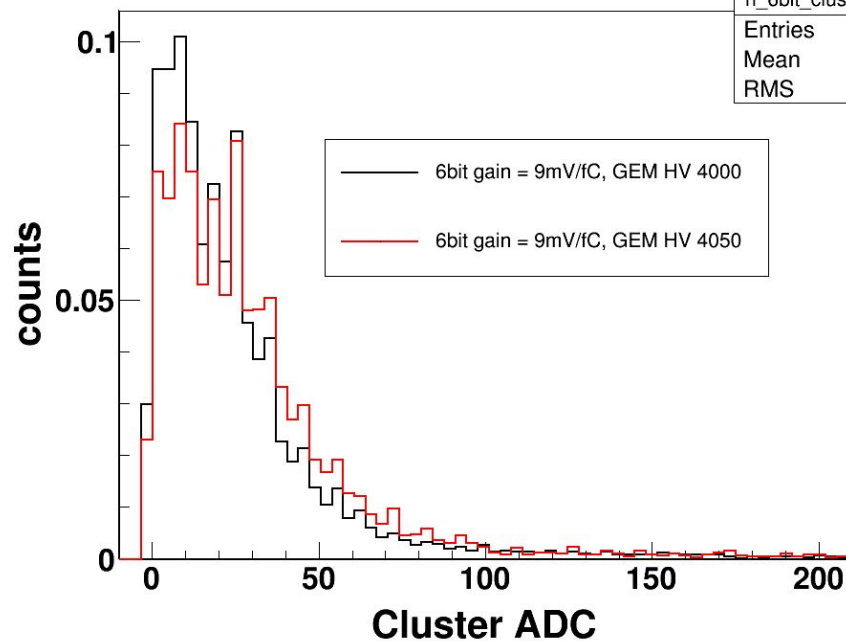


Noise vs signal with different GEM HV

- GEM gain already reached plateau at HV = 4000 V

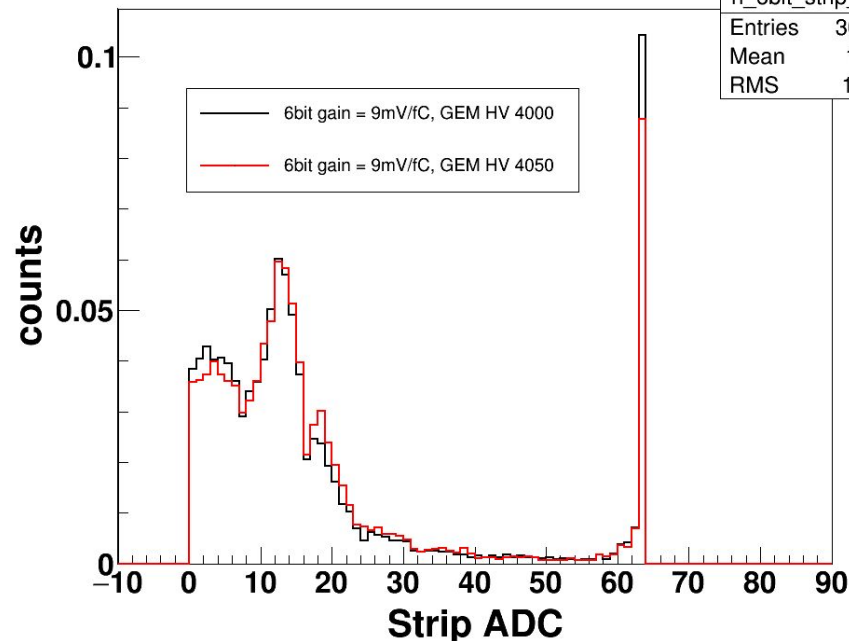
cluster ADC distribution

h_6bit_cluster_adc	
Entries	11910
Mean	24.98
RMS	27.6

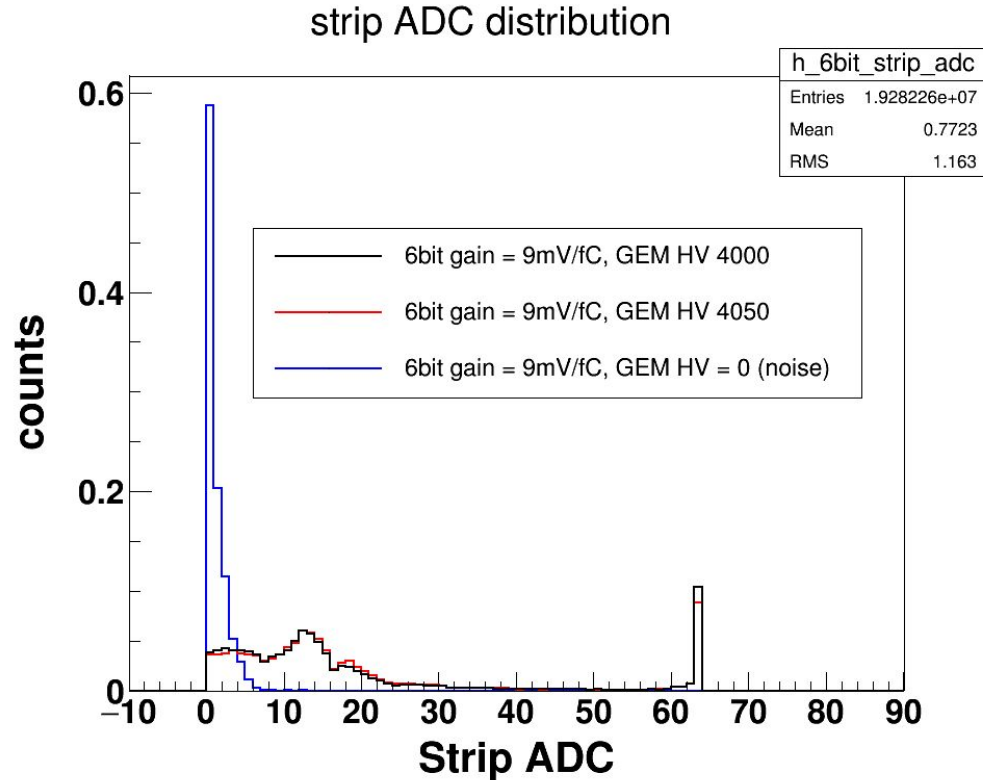


strip ADC distribution

h_6bit_strip_adc	
Entries	30342
Mean	18.01
RMS	18.85

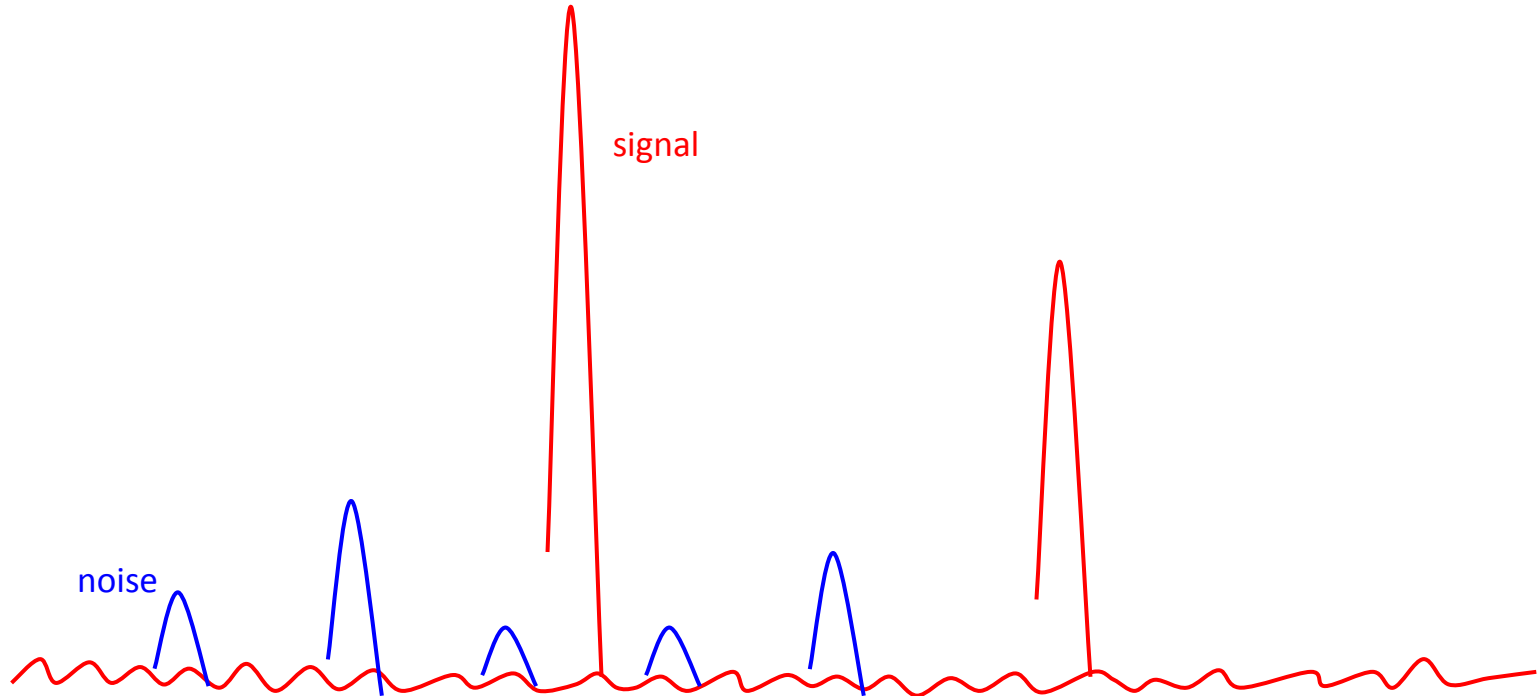


Noise vs signal with different GEM HV



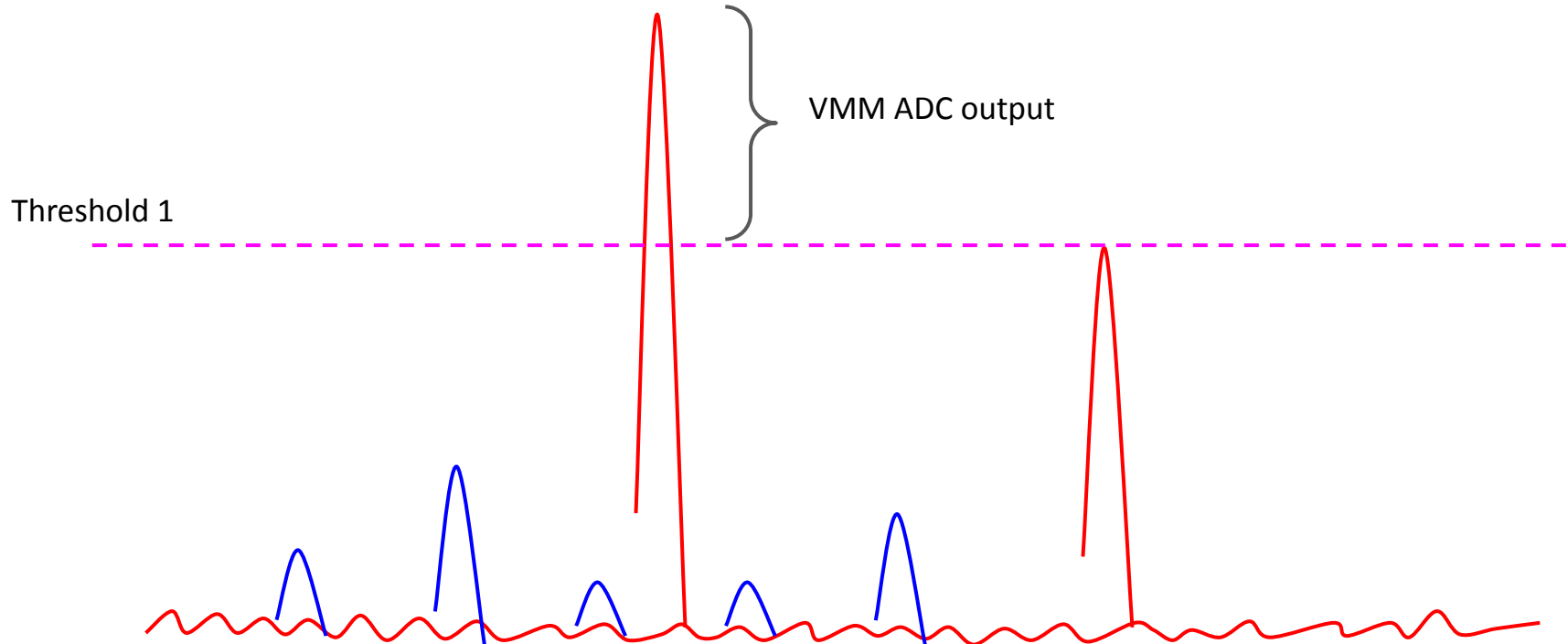
All signal is already threshold subtracted by the VMM chip

- ADC output from VMM already have threshold subtracted



All signal is already threshold subtracted by the VMM chip

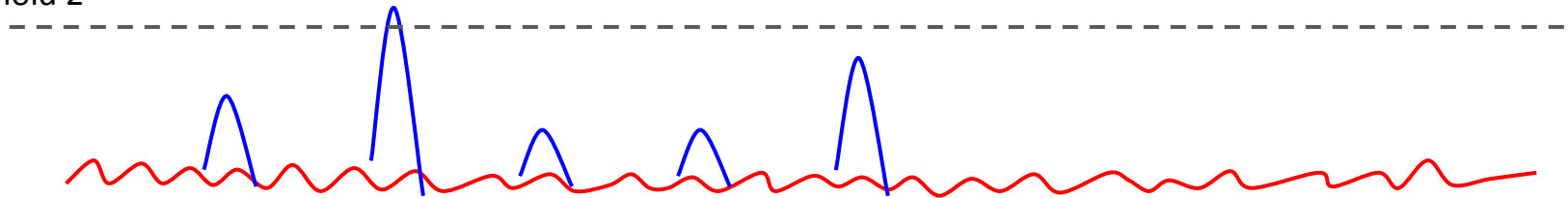
- ADC output from VMM already have threshold subtracted



All signal is already threshold subtracted by the VMM chip

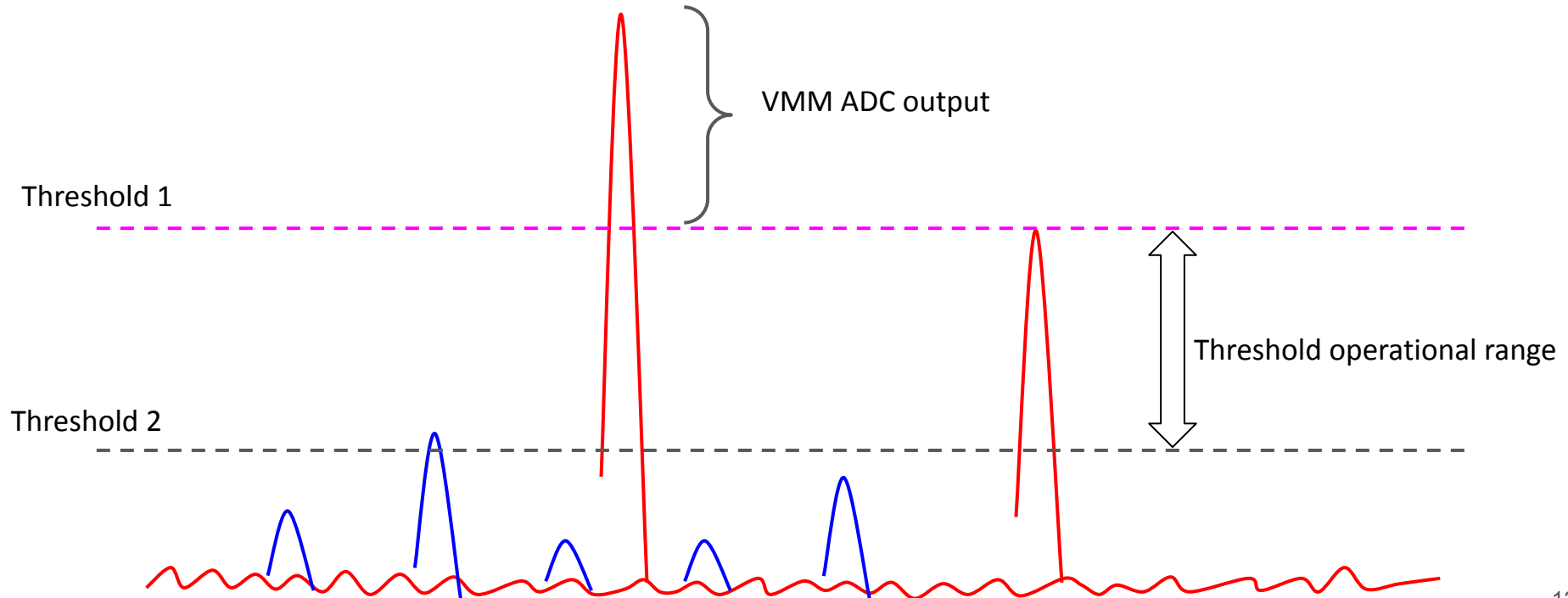
- ADC output from VMM already have threshold subtracted

Threshold 2



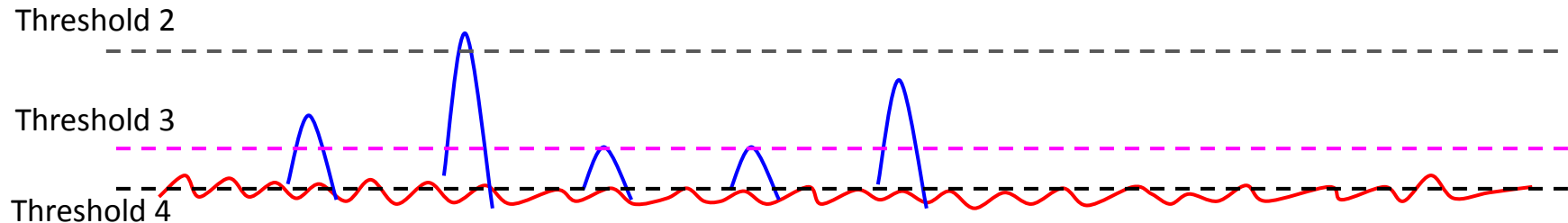
All signal is already threshold subtracted by the VMM chip

- ADC output from VMM already have threshold subtracted



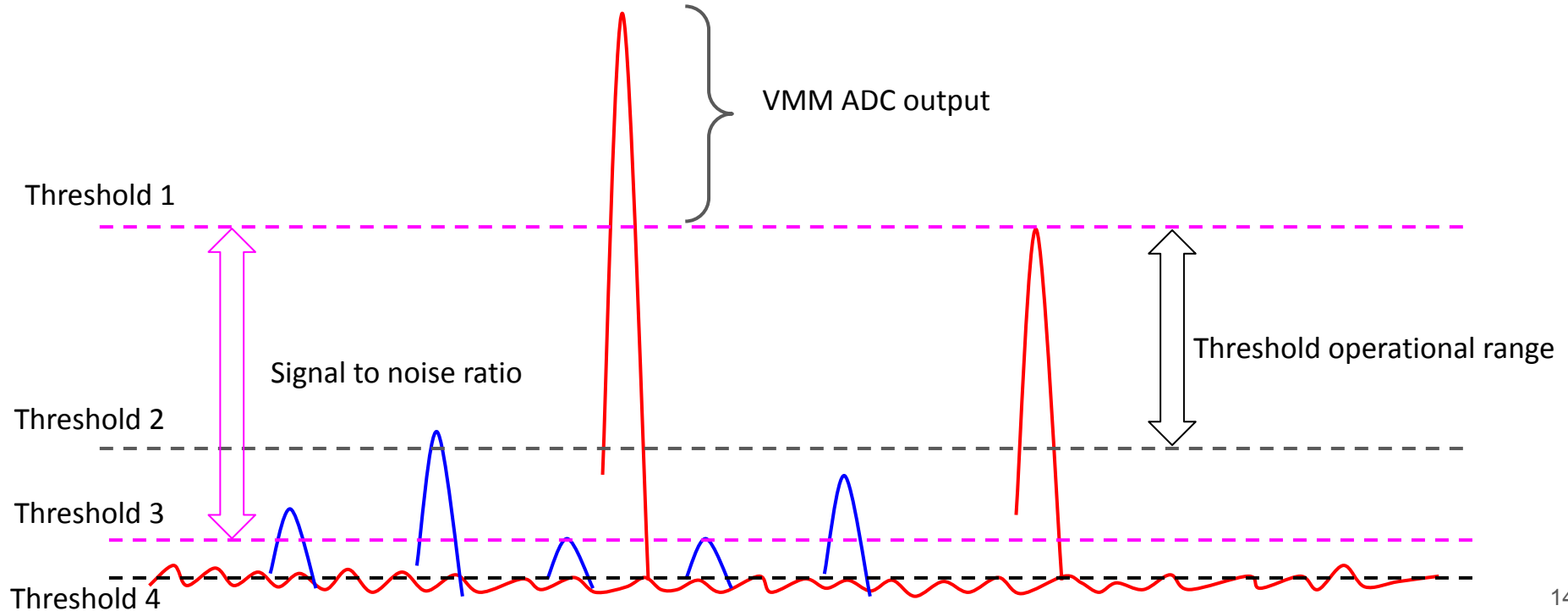
All signal is already threshold subtracted by the VMM chip

- ADC output from VMM already have threshold subtracted



All signal is already threshold subtracted by the VMM chip

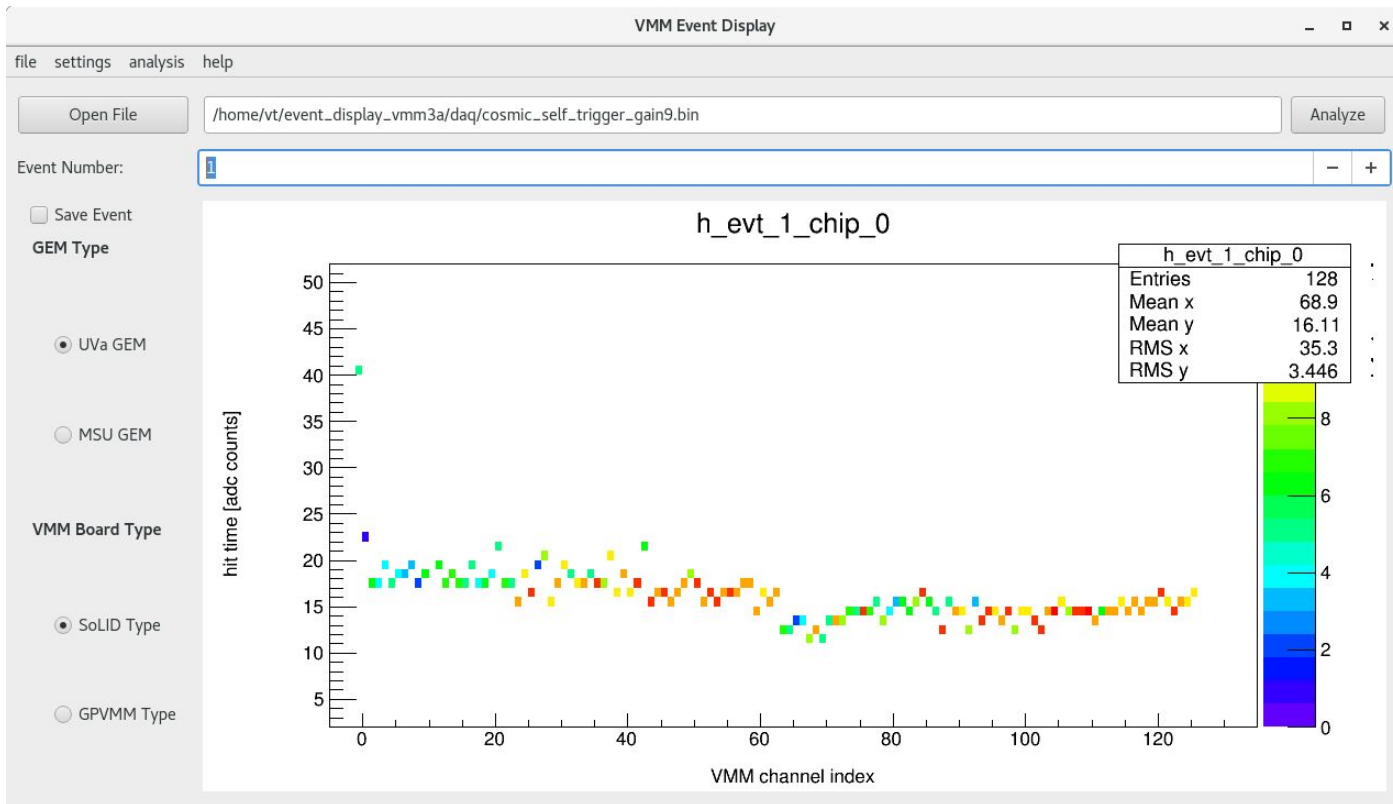
- ADC output from VMM already have threshold subtracted



Effective way to estimate the distance between signal and noise

- Using DAC level to estimate signal amplitude
- We don't see any signal with threshold at $\text{DAC} = 250$, so start from $\text{DAC} = 250$, within a fixed amount of running time:
 - Put GEM HV = 0, **decrease** the DAC level until we reach the maximum hit count, this DAC (DAC_{low}) indicate noise signal amplitude
 - Put GEM on HV, **increase** the threshold until we start to see the hit count drops, this DAC (DAC_{high}) indicate MIP signal value
 - Then the distance between MIP signal and noise is: **$\text{DAC}_{\text{high}} - \text{DAC}_{\text{low}}$**
 - VMM equipped a 12-bit DAC
 - At present, this difference is estimated to be > 80 DAC counts
 - This distance is independent of which VMM mode we use (6-bit, 8-bit, 10bit, L0, continuous...)

Another issue



We got the GPVMM board working, we can compare with GPVMM board with SoLID board

Outline

- How we determine noise signal amplitude
- Threshold vs counts plot
- Noise amplitude with different gain - noise independent of gain setting
- Noise amplitude with different threshold
- Noise level vs GEM cosmic signal ADC distribution (with different GEM HV)
- Self-triggering mode issue

All signal is already threshold subtracted by the VMM chip

- ADC output from VMM already have threshold subtracted

