

SBS-DAQ-GMn-HCal - Feature #617

Re-configure F1 clock to use TI clock

03/29/2021 04:12 PM - Juan Carlos Cornejo

Status:	New	Start date:	03/29/2021
Priority:	Normal	Due date:	
Assignee:		% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:		Spent time:	0.00 hour

Description

Notes from Bryan Moffit:

I took a look at what you need to connect the TI 31.25MHz clock to the F1 FrontPanel SD.

There's some TI switches that need to be set. They're near the top of the module, if you're looking at the PCB with the VME connectors on the right:

(See Attachment #1 - TI_TDC_Switches).

The TDC connector on the TI has these pin outs. Those 4 signals need connected to the f1 front panel SD (See Attachment #2 - TI TDC Table)

You'll connect the clock to Connector A (14,13) and the rest of the signals to/from connector B on the F1 front panel SD. (See Attachment #3 F1v1 TDC Input Output Table).

History

#1 - 03/29/2021 07:53 PM - Alexandre Camsonne

- Project changed from SuperBigBite-detector to SBS-DAQ-GMn-HCal

Files

TI_TDC_switches.png	108 KB	03/29/2021	Juan Carlos Cornejo
TI_TDC.png	42.6 KB	03/29/2021	Juan Carlos Cornejo
f1tdc_fpsd.png	67.1 KB	03/29/2021	Juan Carlos Cornejo